

<b>Notice of References Cited</b>	Application/Control No. 10/829,626		Applicant(s)/Patent Under Reexamination BARSNESS ET AL.	
	Examiner Caroline Arcos		Art Unit 2195	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,691,067 B1	02-2004	Ding et al.	702/186
*	B	US-2004/0025146 A1	02-2004	Kurihara et al.	717/131
*	C	US-6,820,207 B2	11-2004	Dawkins et al.	713/324
*	D	US-2005/0086660 A1	04-2005	Accapadi et al.	718/107
*	E	US-6,970,999 B2	11-2005	Kurihara et al.	712/227
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"An assembly-level execution- Time model for pipelined architectures", IEEE, 2001, Pages 195, 200□□□□
	V	"Performance analysis and its impact on design", IEEE, 1998, Pages 41-49
	W	"Instruction level profiling and evaluation of the IBM RS/6000", ACM, 1991, Pages 180-189
	X	"Slicing the AS/400 with logical partitioning: A how to guide, IBM, 1999, Pages 1-218.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.